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Attorney's Docket No.: 10559-738001 / P13587

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E. Hall

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : George P. Merrill et al. /
Serial No. : 10/060,865 /
Filed : January 29, 2002 /
Title : QUEUE MANAGEMENT /

Art Unit : Unknown
Examiner : Unknown

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

In the Specification:

Replace the paragraph beginning at page 2, line 18 with the following rewritten paragraph:

A₄ -- Typically, slave processors 14, 16 and 18 are programmable state machines that control the flow of data retrieved from a data port or bus. For example, slave processor 14 may control the transmission and retrieval of data on a WAN (wide-area network) and slave processors 16 and 18 may control the transmission and retrieval of data on LANs (local-area networks), such as 10BaseT and 100BaseTx Ethernet networks.- -

In the Claims:

Add claim 31-40.

A₂ 31. Circuitry to determine at least one queue parameter for a process to be run on a system, said at least one queue parameter to be used to configure one or more queues, said circuitry comprising:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

April 22, 2002
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